

10

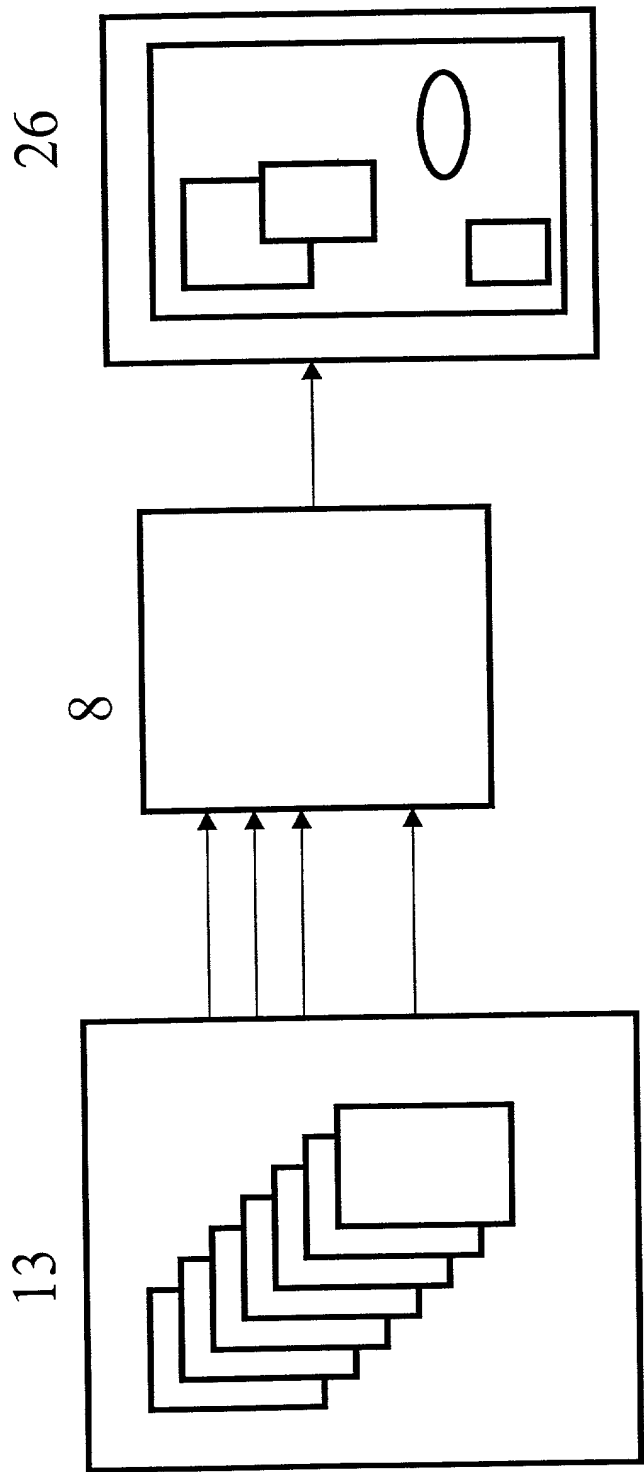


Fig. 1

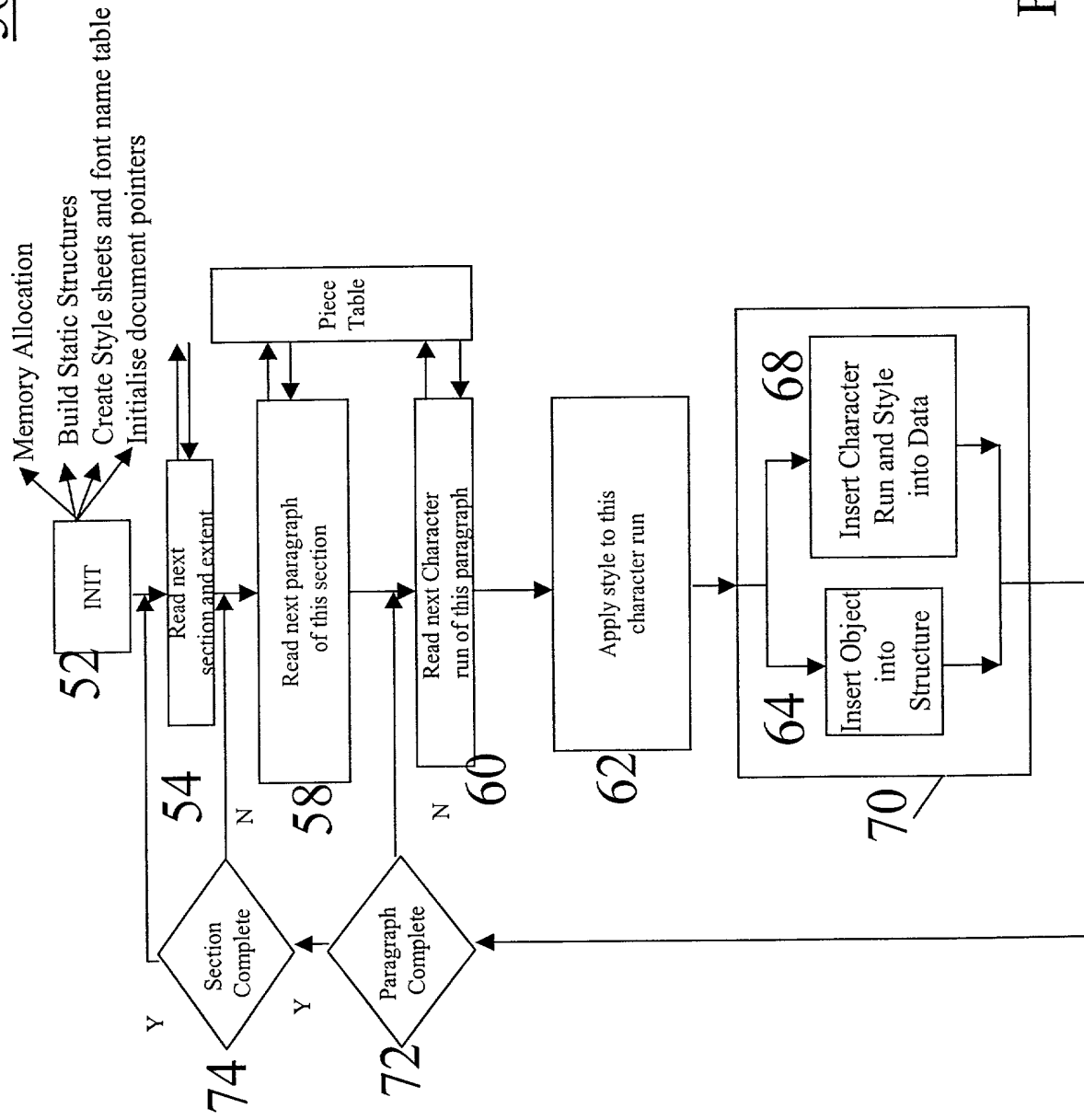


Fig. 3

32

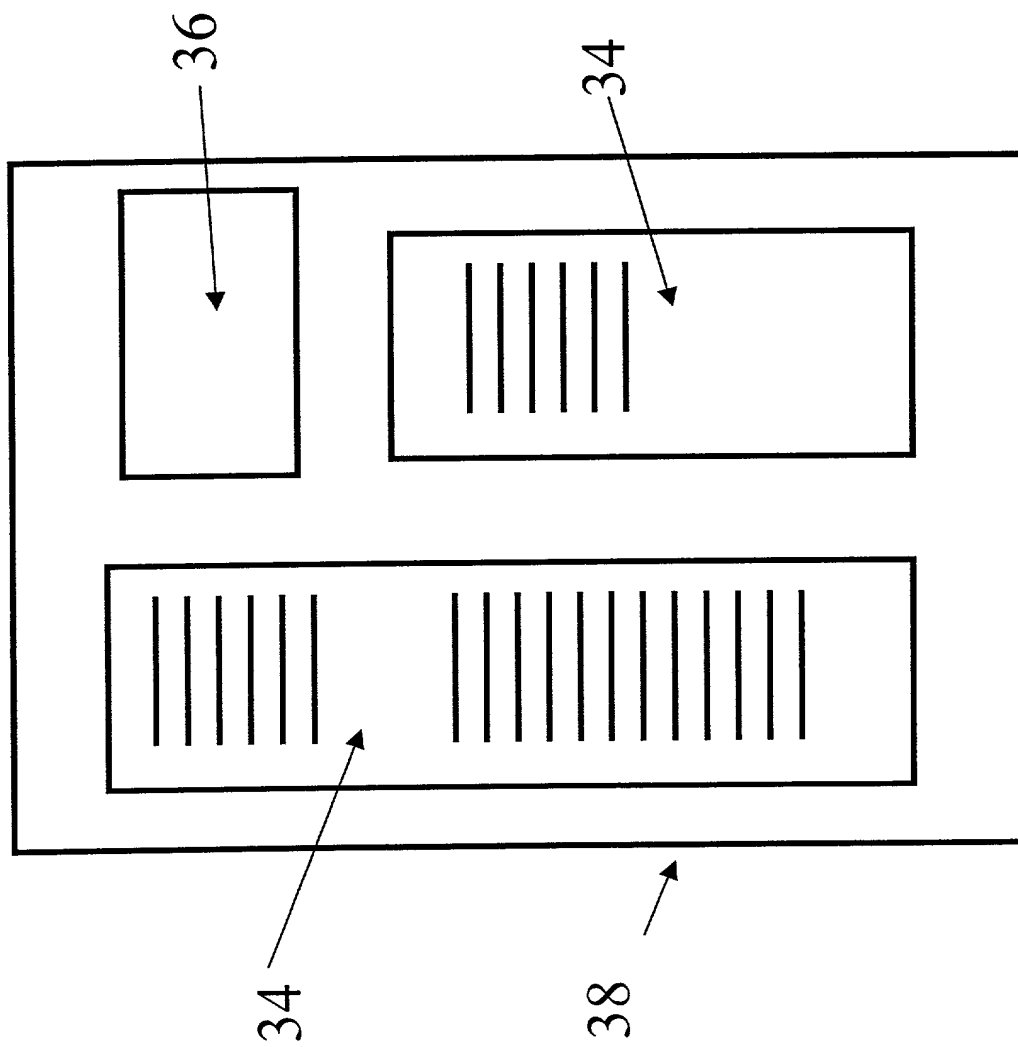


Fig. 4

Fig. 5a

80

82

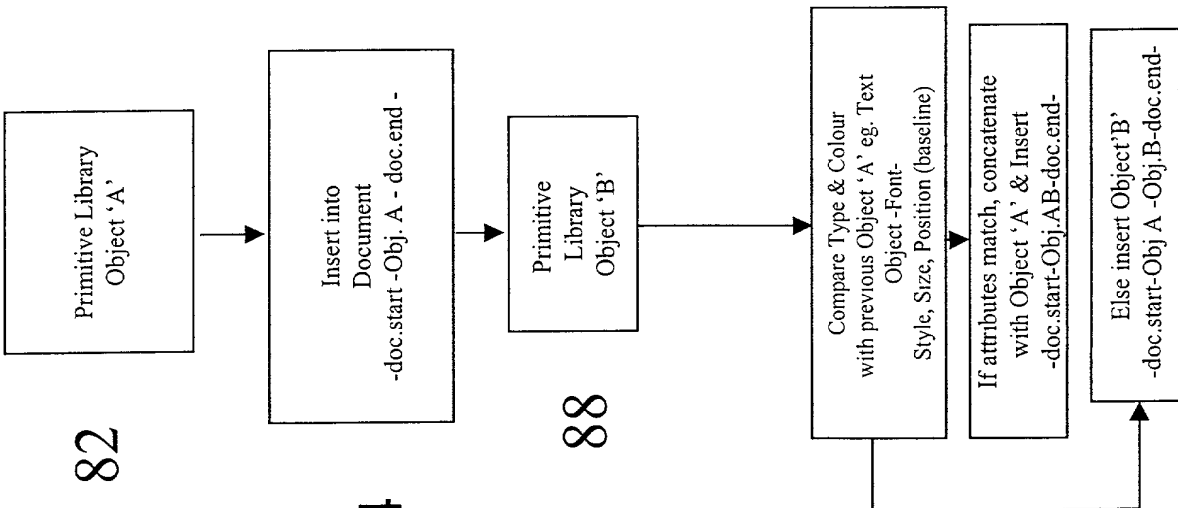
84

88

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94

98



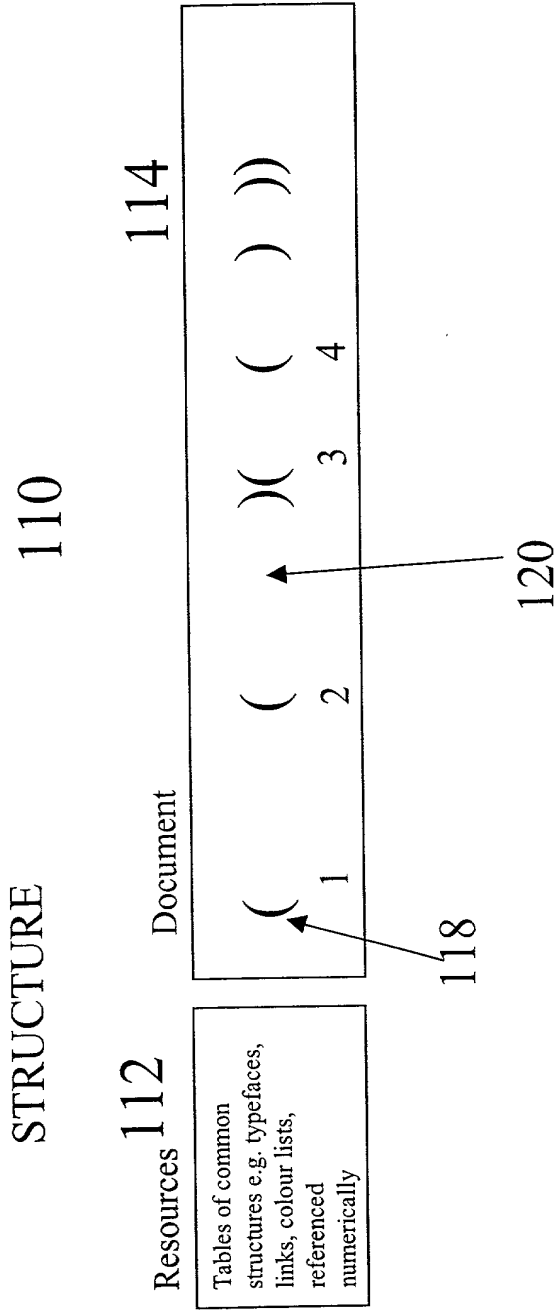


Figure 6

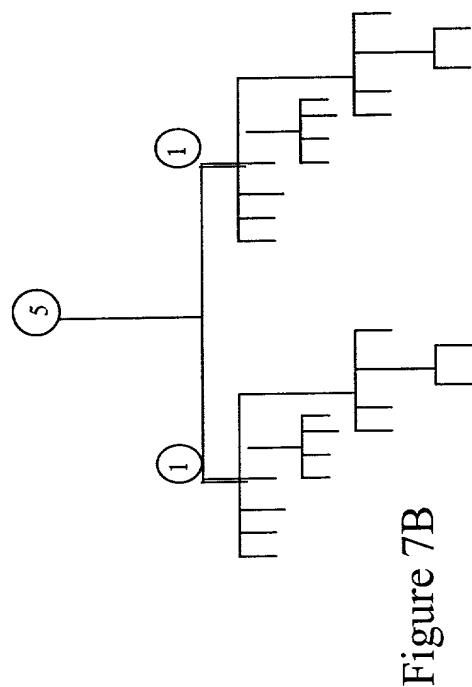
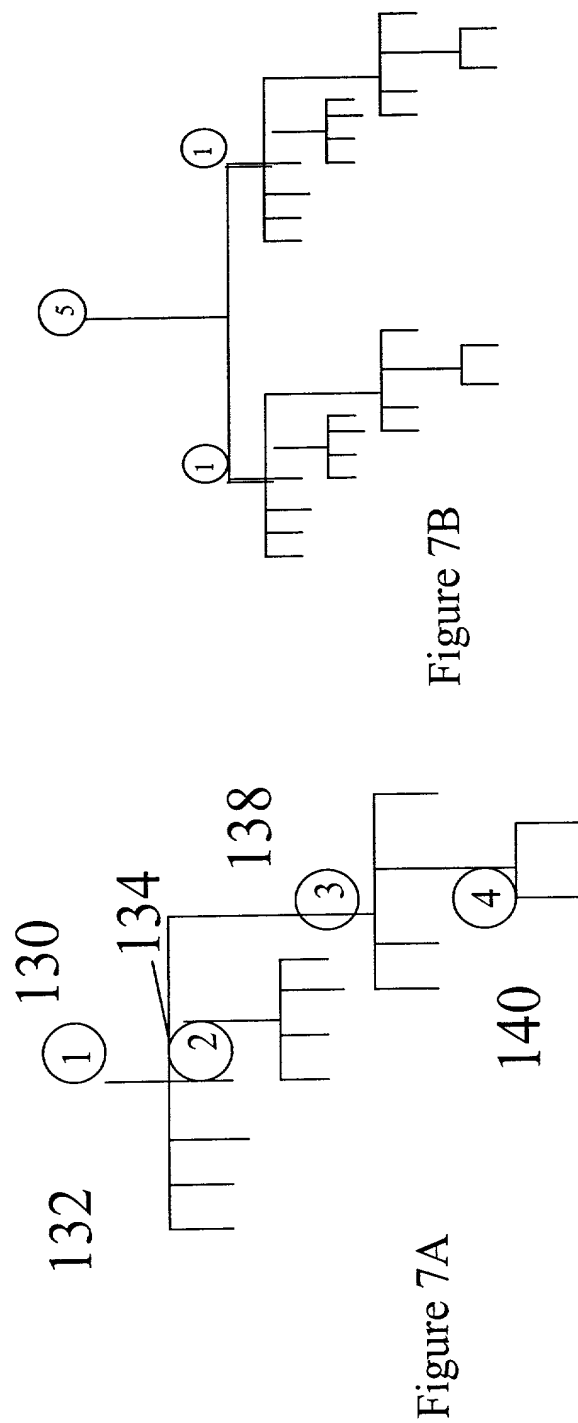


Diagram illustrating a memory layout structure:

- Indirection List (154):** A table mapping object numbers to offsets.

Obj Num	1	2	3	17	42
Offset	700	810	1024	8000	17321
- Data (150):** A memory block containing object data.

Object 1 Data	Object 2 Data	Object 3 Data	Object 17 Data	Object 42 Data
700	810	1024	8000	17321